

FIG. 1

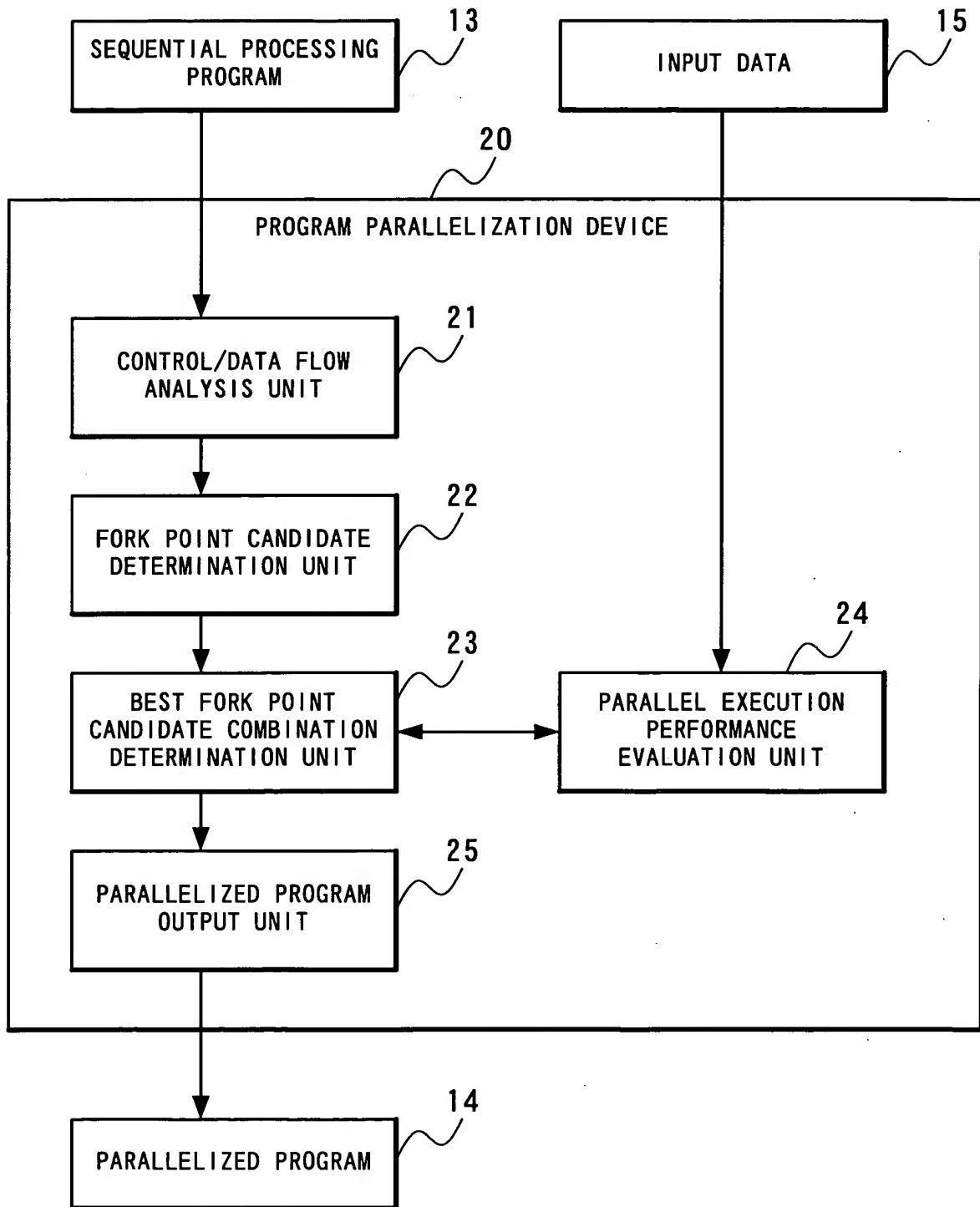


FIG. 2

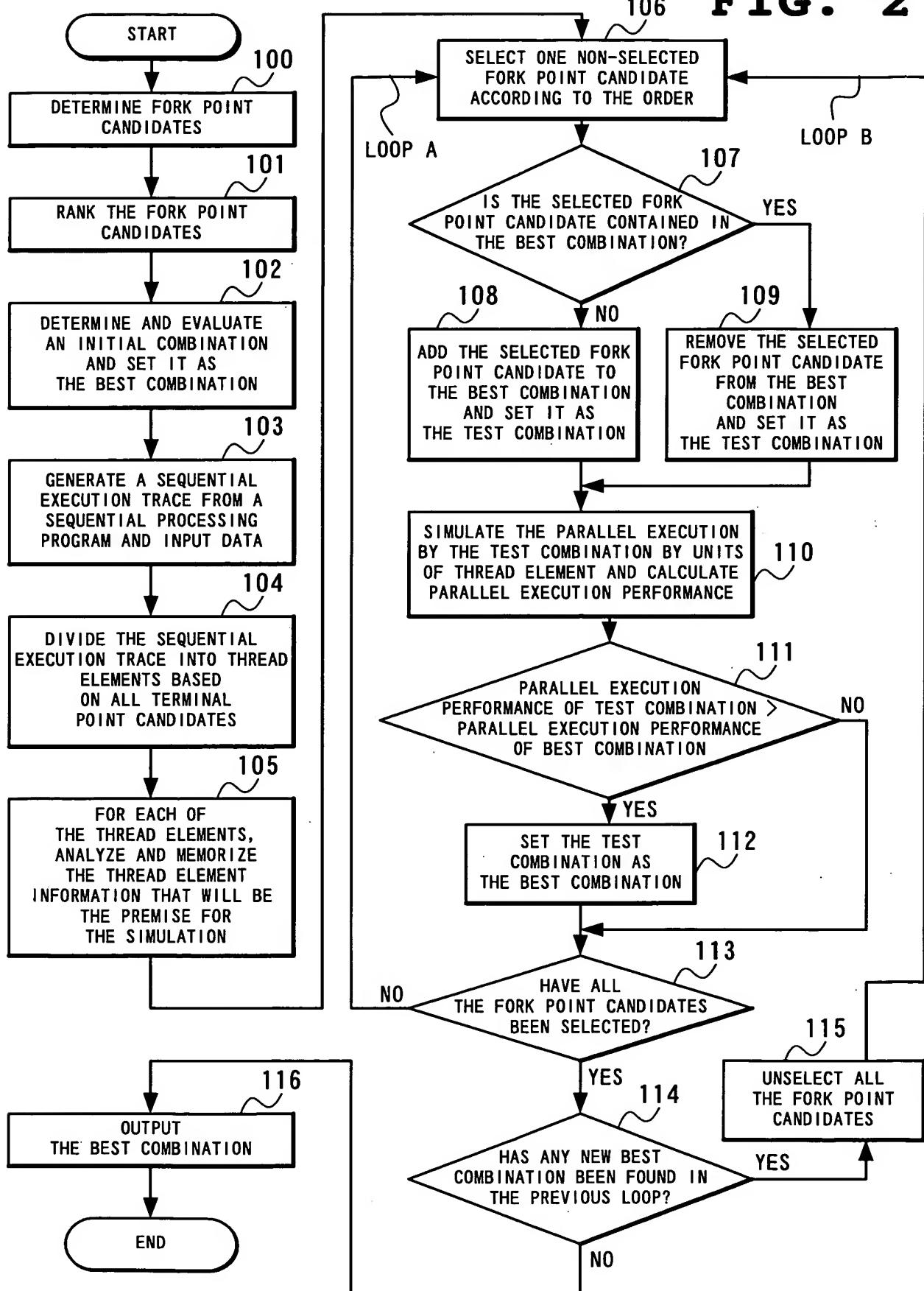


FIG. 3

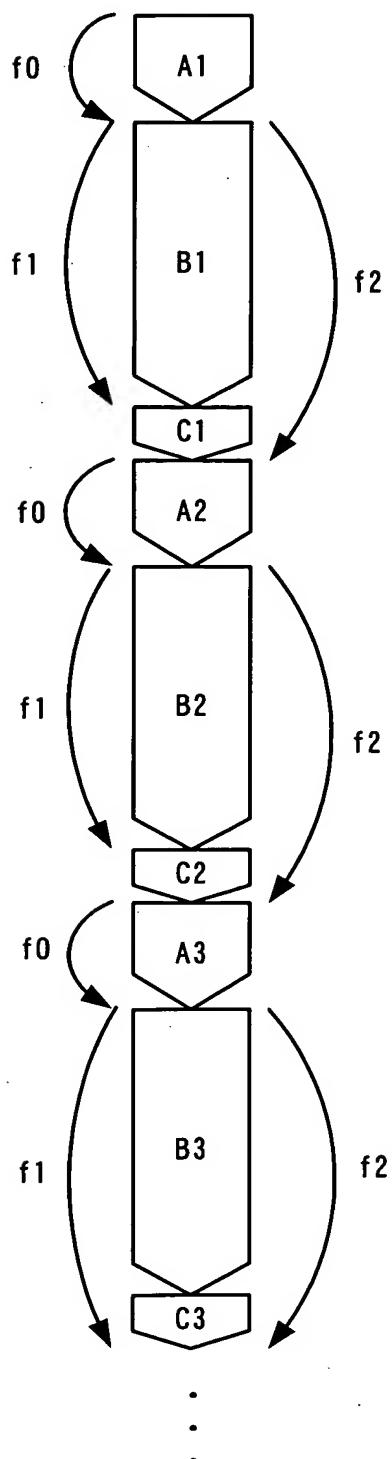


FIG. 4

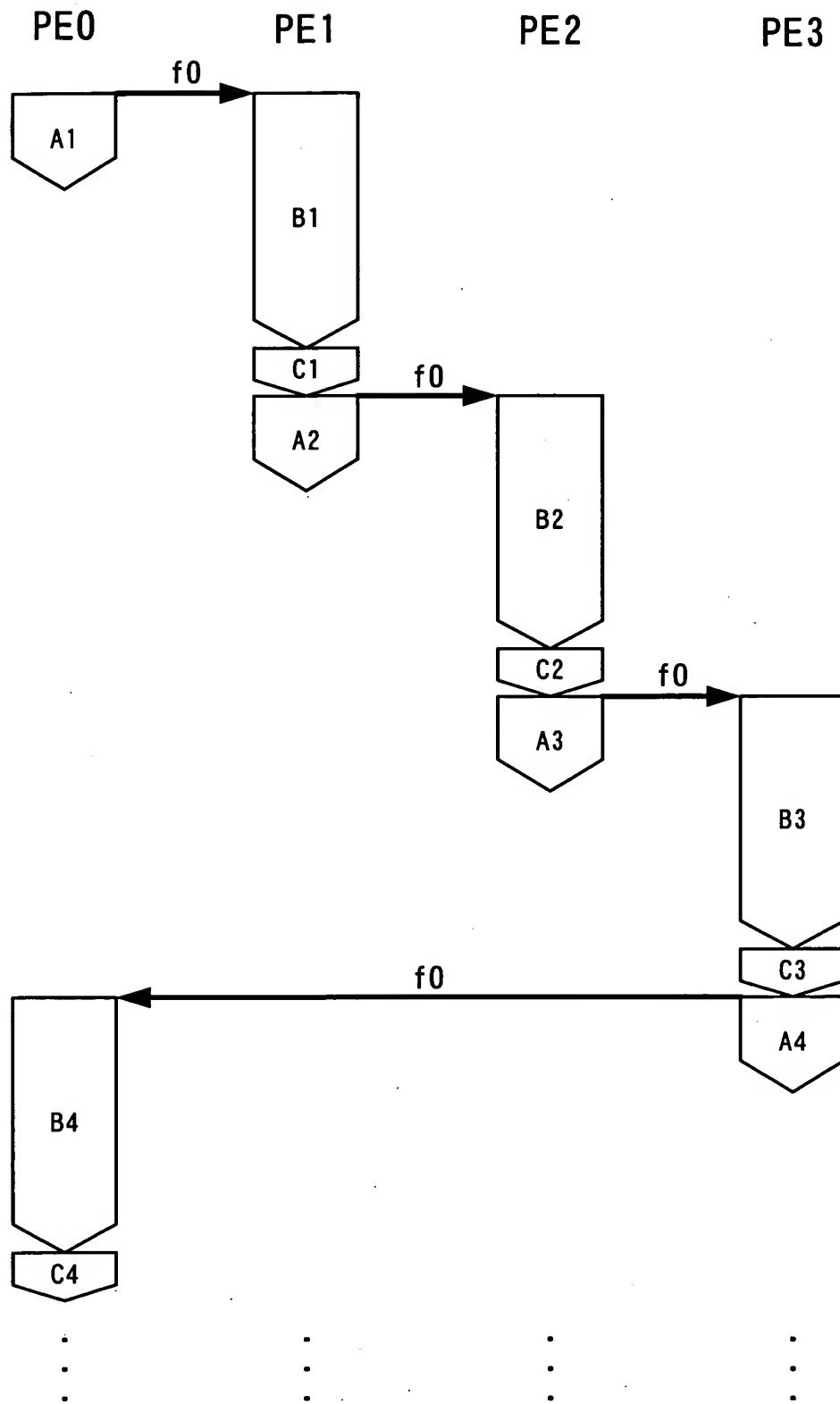


FIG. 5

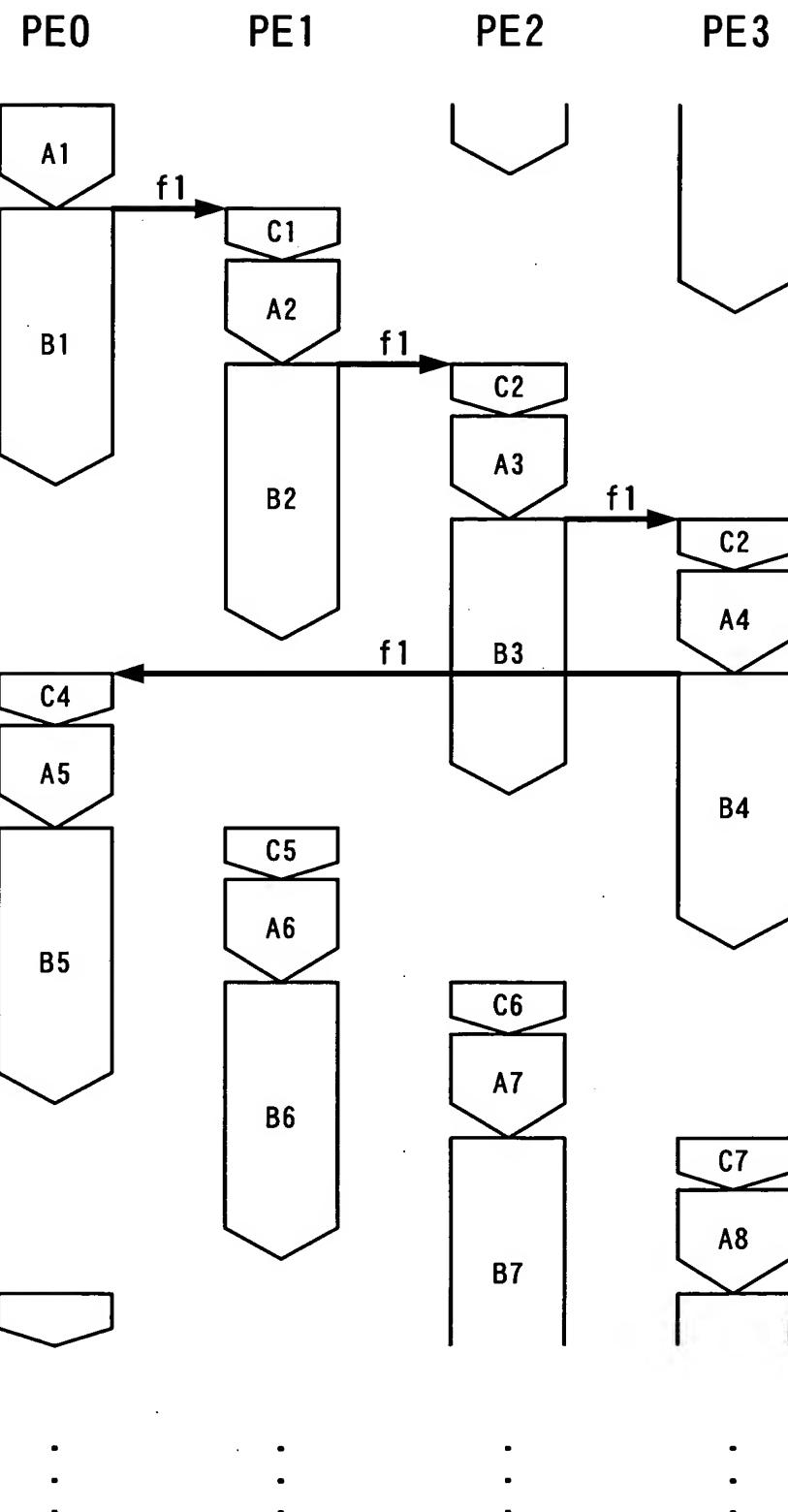


FIG. 6

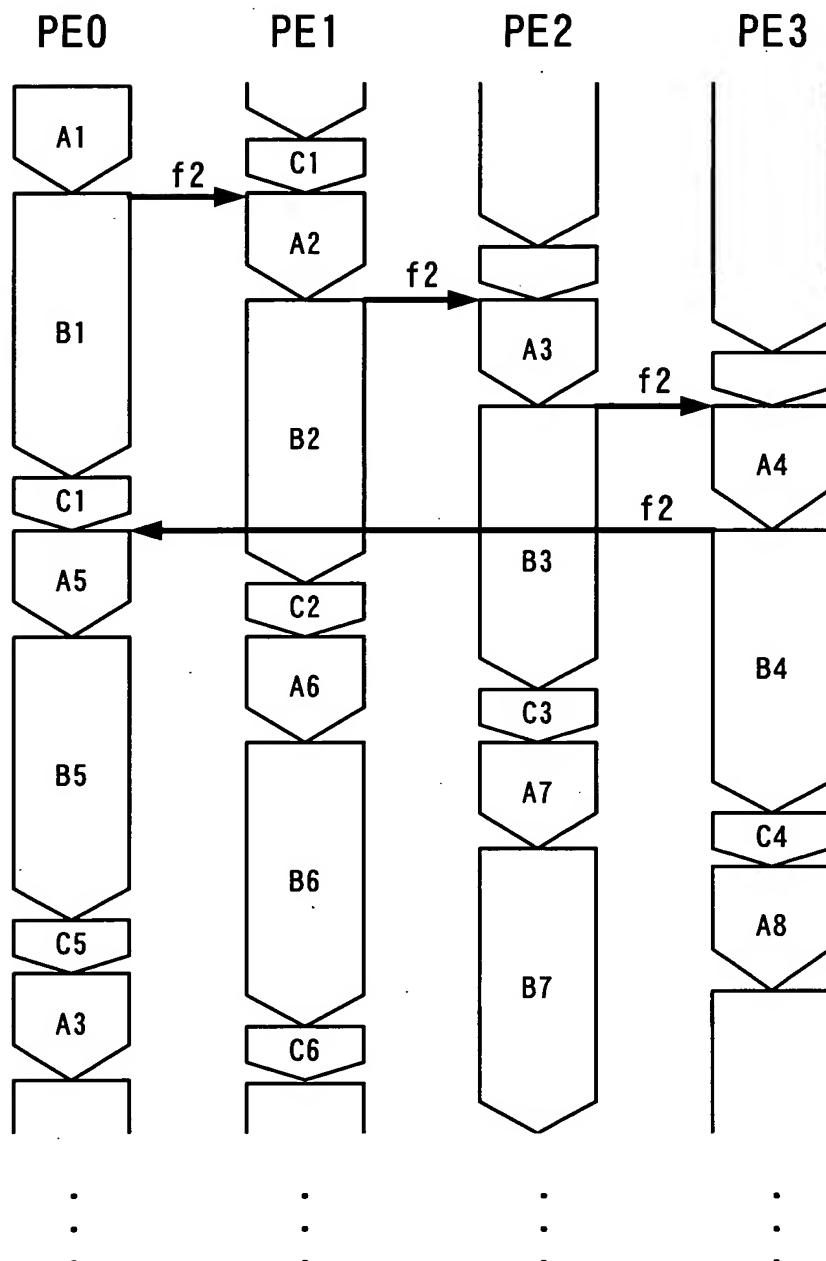


FIG. 7

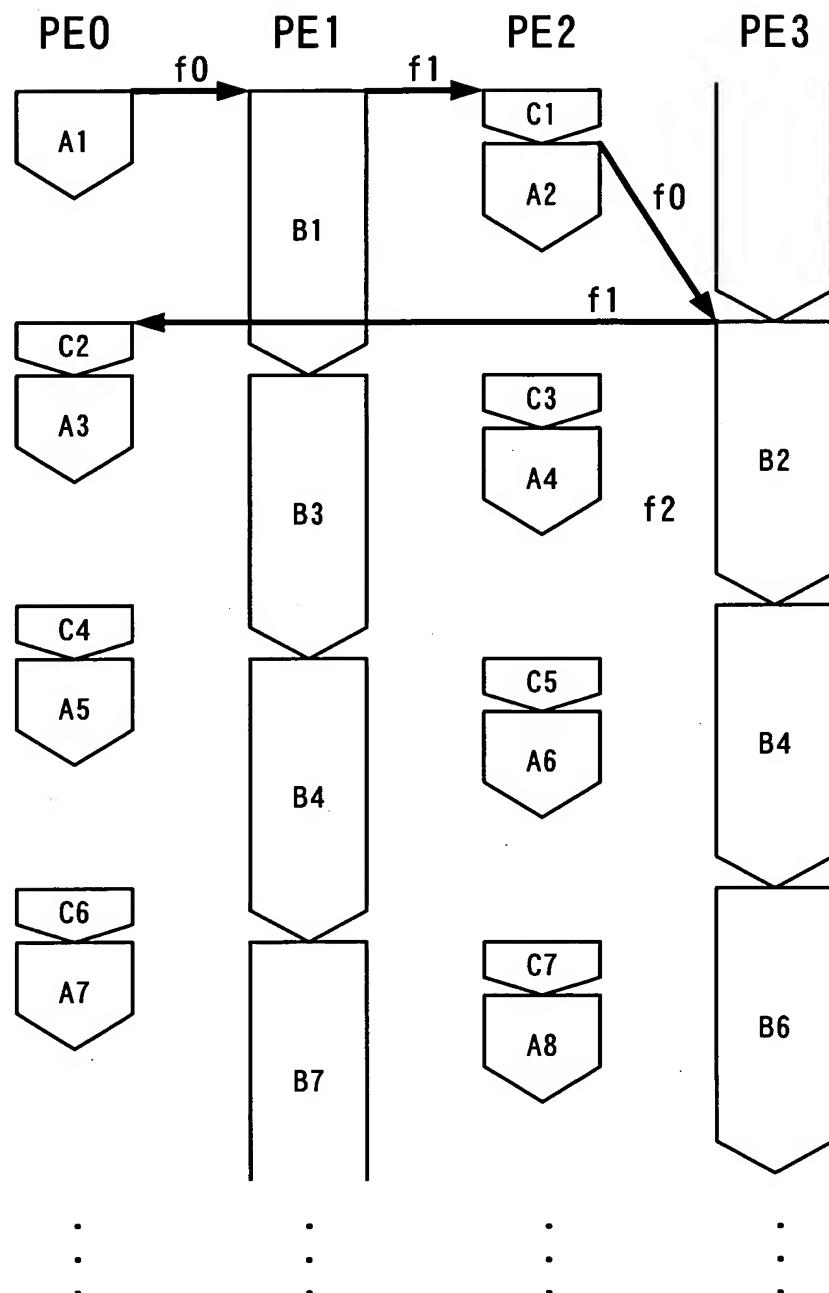


FIG. 8

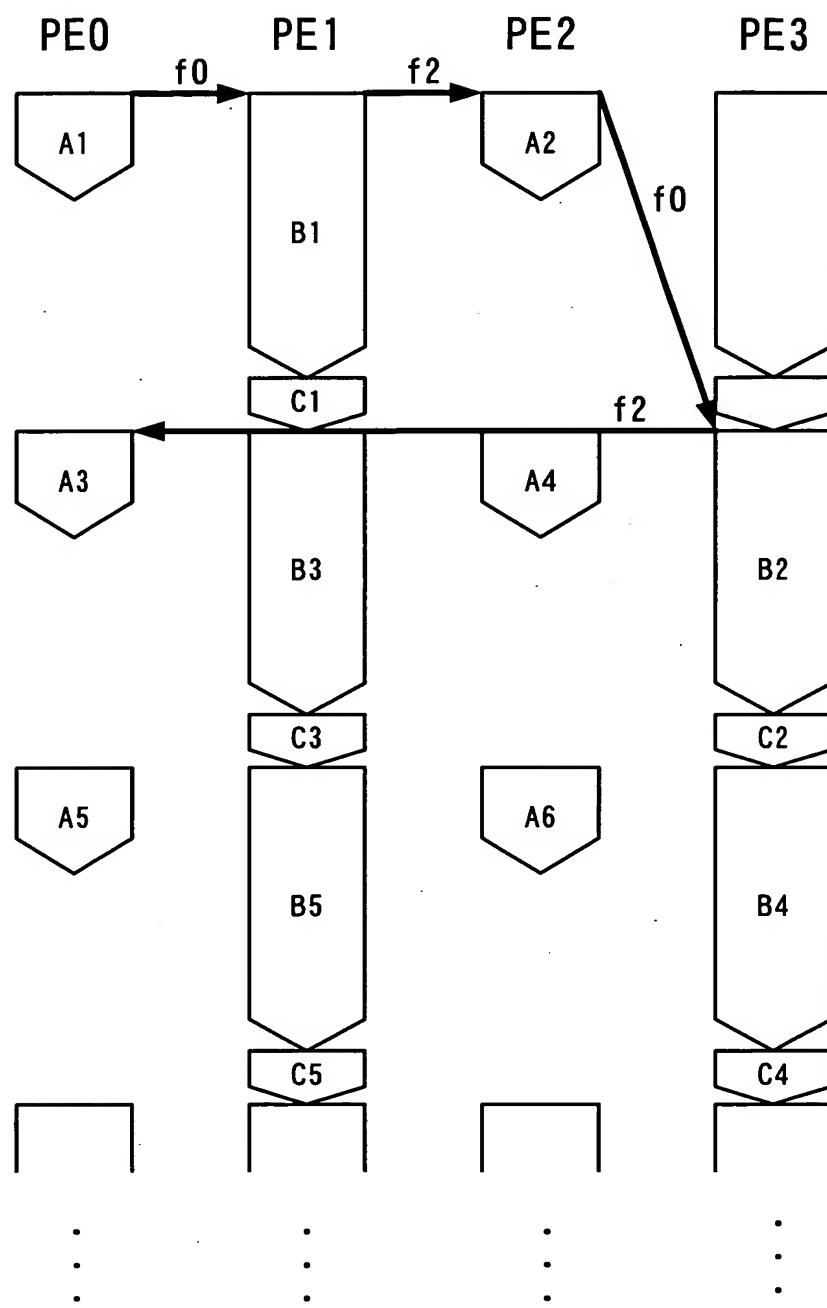


FIG. 9

FORK POINT CANDIDATE COMBINATION	PARALLEL EXECUTION PERFORMANCE (TIMES)
ϕ	1.0
{f0}	2.5
{f1}	2.2
{f2}	2.4
{f3}	1.8
{f0, f1}	3.0
{f0, f2}	3.2
{f0, f3}	2.4
{f1, f2}	3.8
{f1, f3}	2.8
{f2, f3}	2.0
{f0, f1, f2}	3.5
{f0, f1, f3}	2.8
{f0, f2, f3}	2.5
{f1, f2, f3}	2.6
{f0, f1, f2, f3}	2.5

FIG. 10

INITIAL COMBINATION: {f0, f2} (PARALLEL EXECUTION PERFORMANCE: 3.2 TIMES)

SELECTED FORK POINT CANDIDATE	TEST COMBINATION	PARALLEL EXECUTION PERFORMANCE	BEST COMBINATION	SELECTED FORK POINT CANDIDATE
ITERATION 1-1 f0	{f2}	2.4	{f0, f2}	f0
ITERATION 1-2 f2	{f0}	2.5	{f0, f2}	f0, f2
ITERATION 1-3 f1	{f0, f1, f2}	3.5	{f0, f1, f2}	f0, f1, f2
ITERATION 1-4 f3	{f0, f1, f2, f3}	2.5	{f0, f1, f2}	f0, f1, f2, f3
ITERATION 2-1 f0	{f1, f2}	3.8	{f0, f1, f2}	f0
ITERATION 2-2 f2	{f1}	2.2	{f1, f2}	f0, f2
ITERATION 2-3 f1	{f2}	2.4	{f1, f2}	f0, f1, f2
ITERATION 2-4 f3	{f1, f2, f3}	2.6	{f1, f2}	f0, f1, f2, f3
ITERATION 3-1 f0	{f0, f1, f2}	3.5	{f1, f2}	f0
ITERATION 3-2 f2	{f1}	2.2	{f1, f2}	f0, f2
ITERATION 3-3 f1	{f2}	2.4	{f1, f2}	f0, f1, f2
ITERATION 3-4 f3	{f1, f2, f3}	2.6	{f1, f2}	f0, f1, f2, f3

FIG. 11

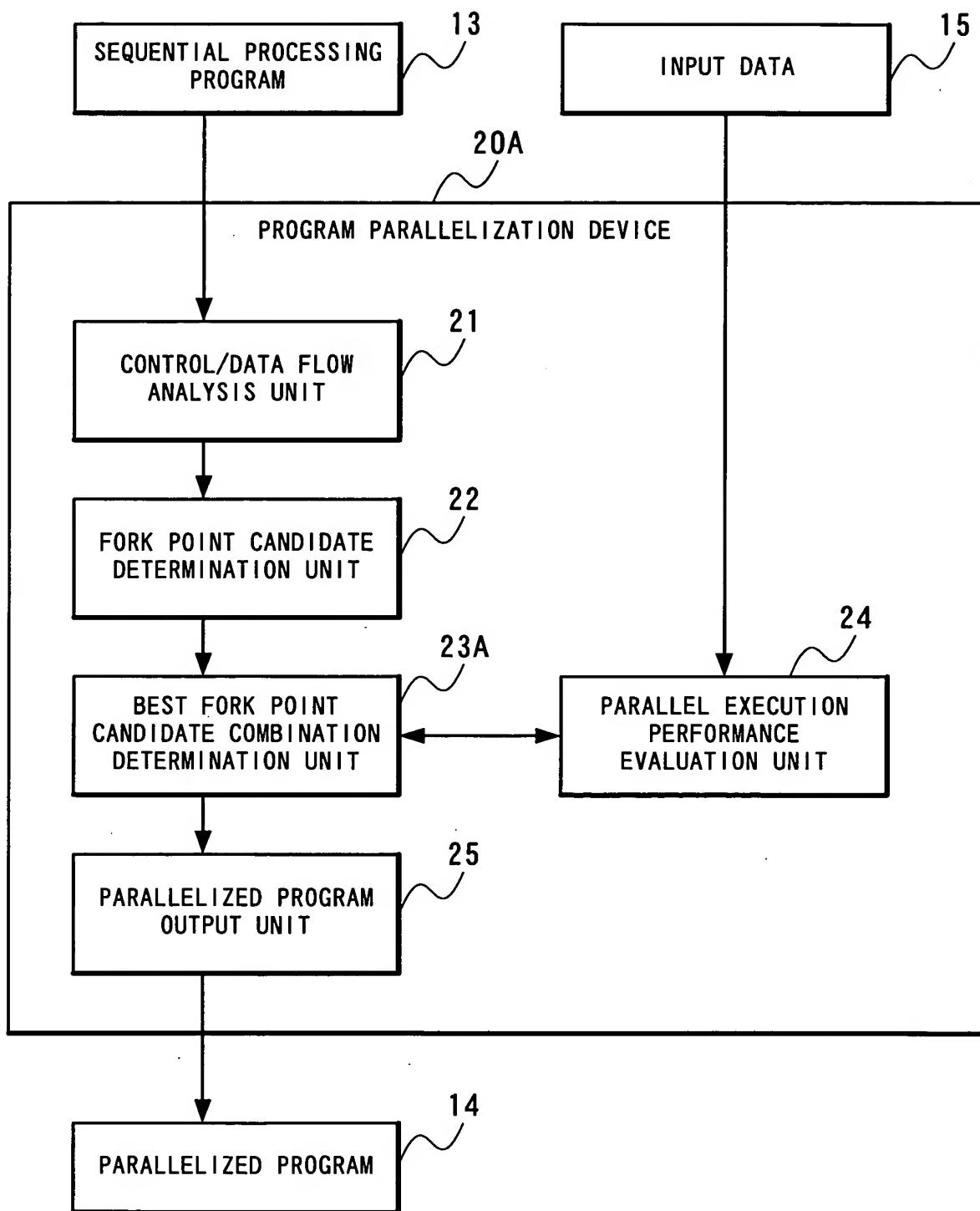


FIG. 12

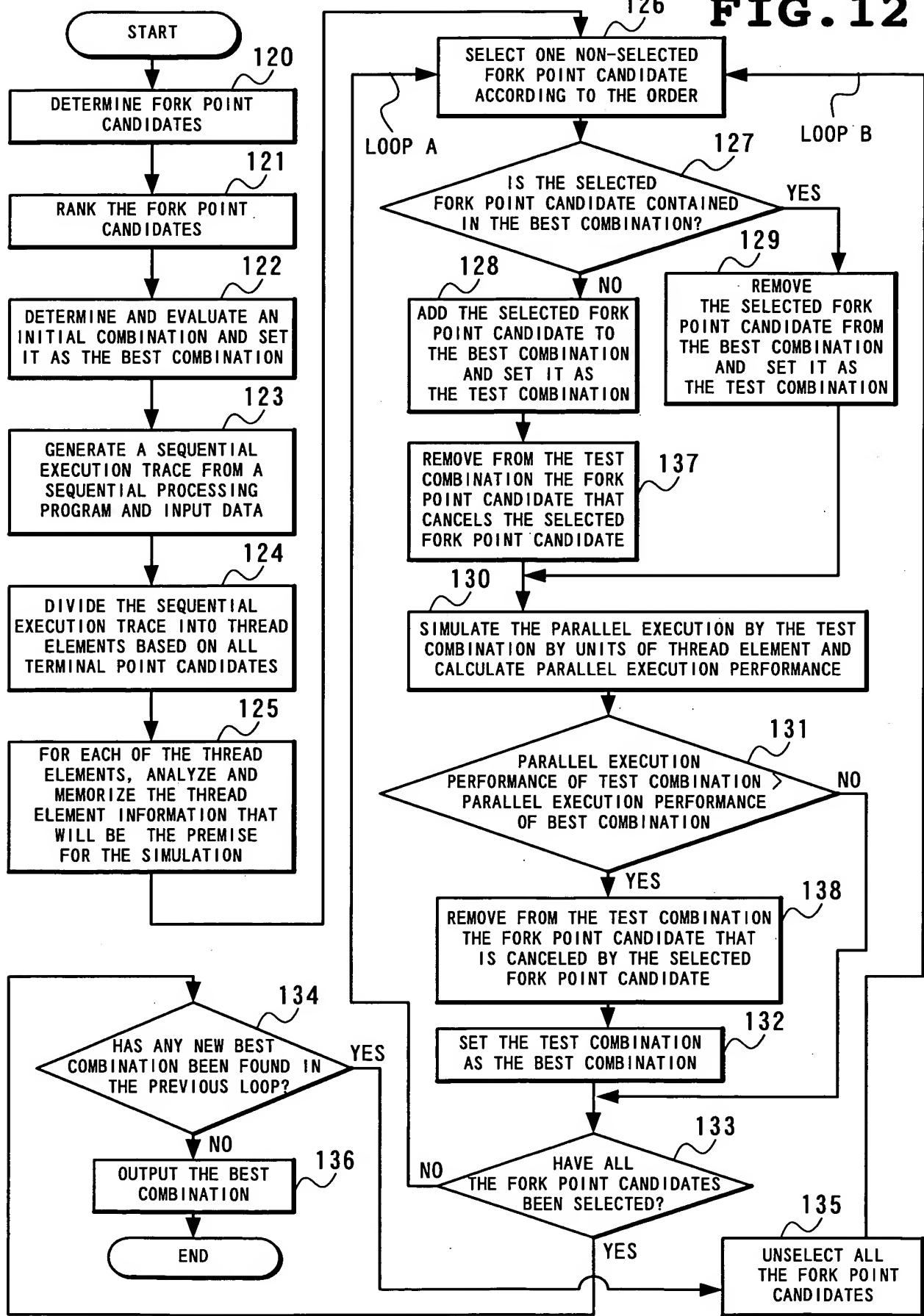


FIG. 13

INITIAL COMBINATION: {f0, f2} (PARALLEL EXECUTION PERFORMANCE: 3.2 TIMES)

	SELECTED FORK POINT CANDIDATE	TEST COMBINATION	PARALLEL EXECUTION PERFORMANCE	BEST COMBINATION	SELECTED FORK POINT CANDIDATE
ITERATION 1-1	f0	{f2}	2.4	{f0, f2}	f0
ITERATION 1-2	f2	{f0}	2.5	{f0, f2}	f0, f2
ITERATION 1-3	f1	{f1, f2}	3.8	{f1, f2}	f0, f1, f2
ITERATION 1-4	f3	{f1, f3}	2.8	{f1, f2}	f0, f1, f2, f3
ITERATION 2-1	f0	{f0, f1, f2}	3.5	{f1, f2}	f0
ITERATION 2-2	f2	{f1}	2.2	{f1, f2}	f0, f2
ITERATION 2-3	f1	{f2}	2.4	{f1, f2}	f0, f1, f2
ITERATION 2-4	f3	{f1, f3}	2.8	{f1, f2}	f0, f1, f2, f3

Title: PROGRAM PARALLELIZATION
 DEVICE, PROGRAM PARALLELIZATION
 METHOD, AND PROGRAM
 PARALLELIZATION PROGRAM
 Inventor(s): Atsufumi SHIBAYAMA et al.
 DOCKET NO.: 040405-0368

FIG. 14

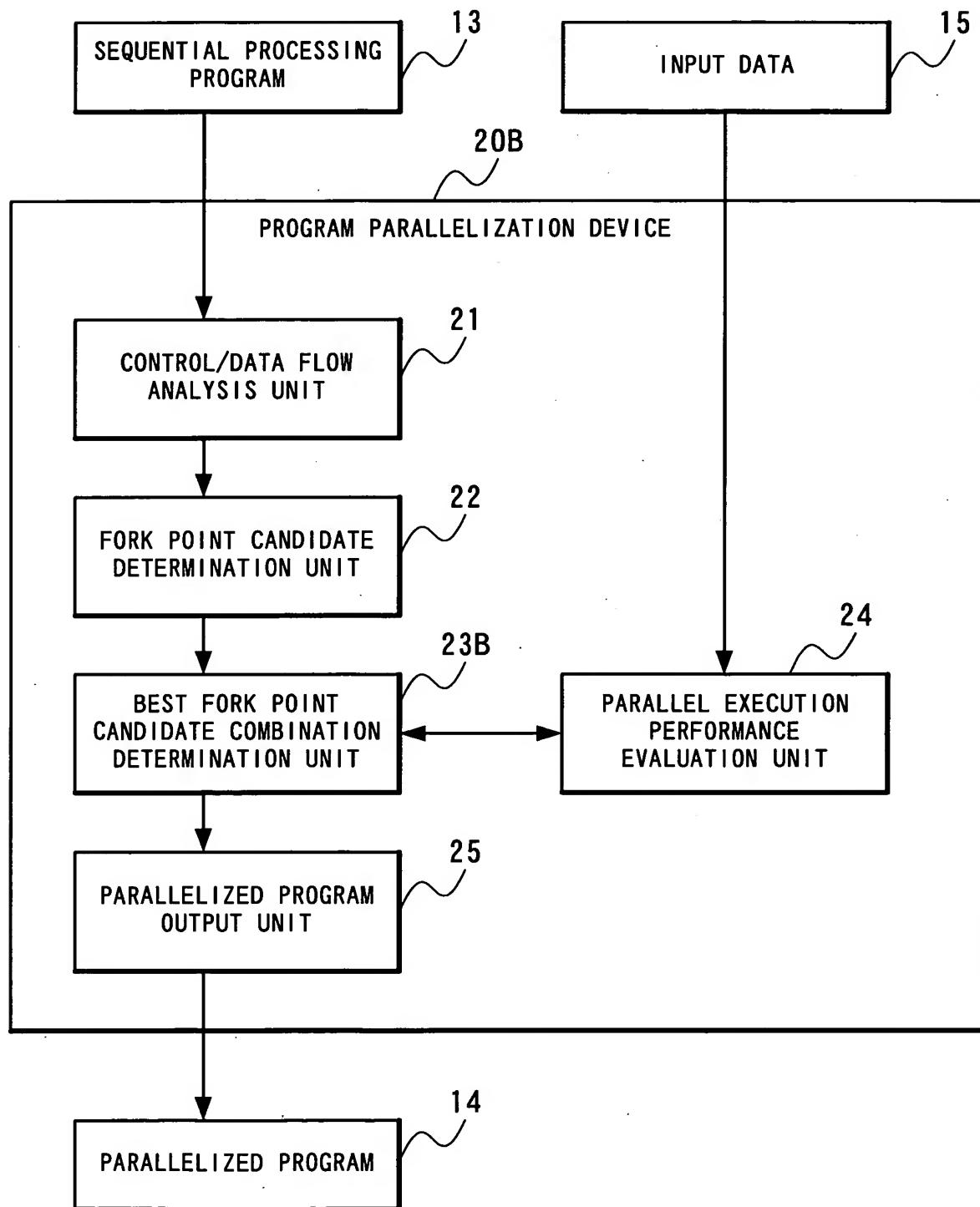


FIG. 15

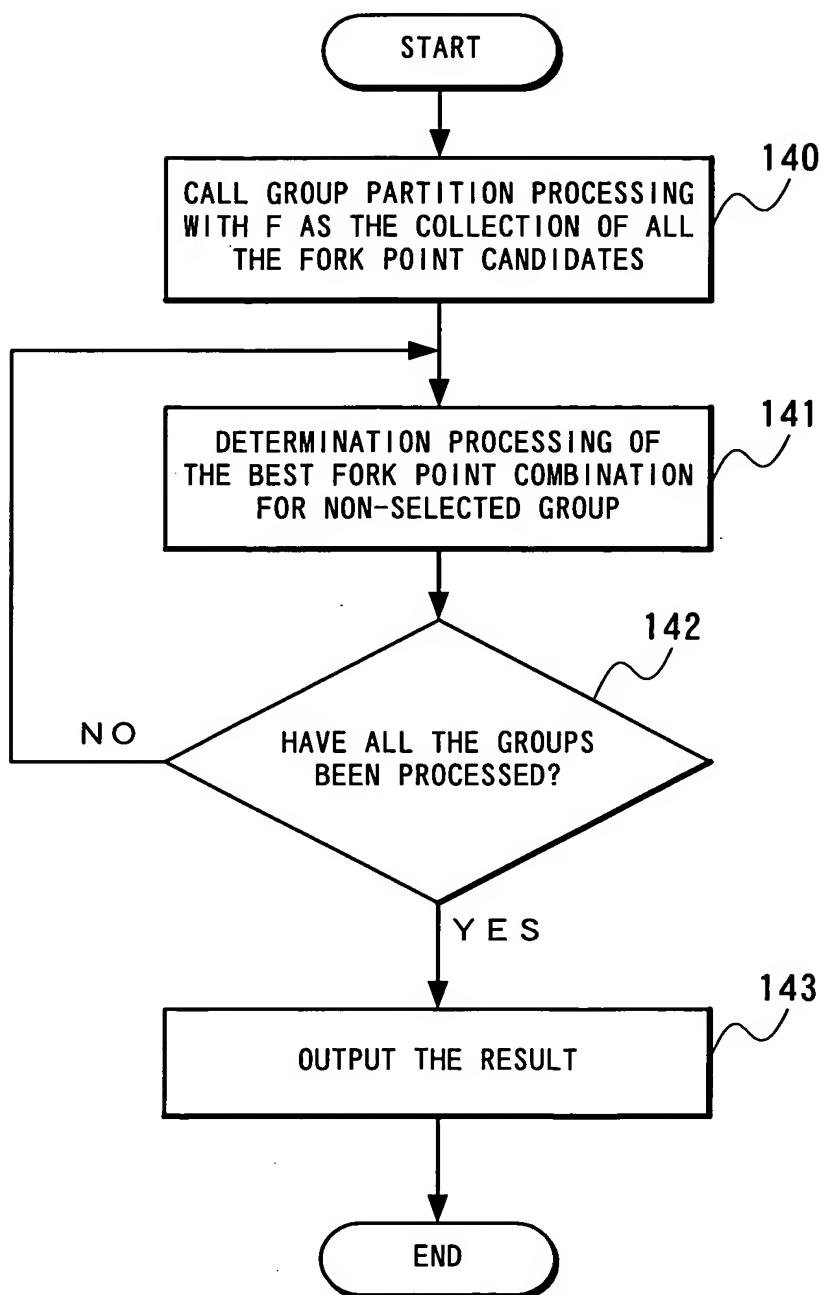


FIG. 16

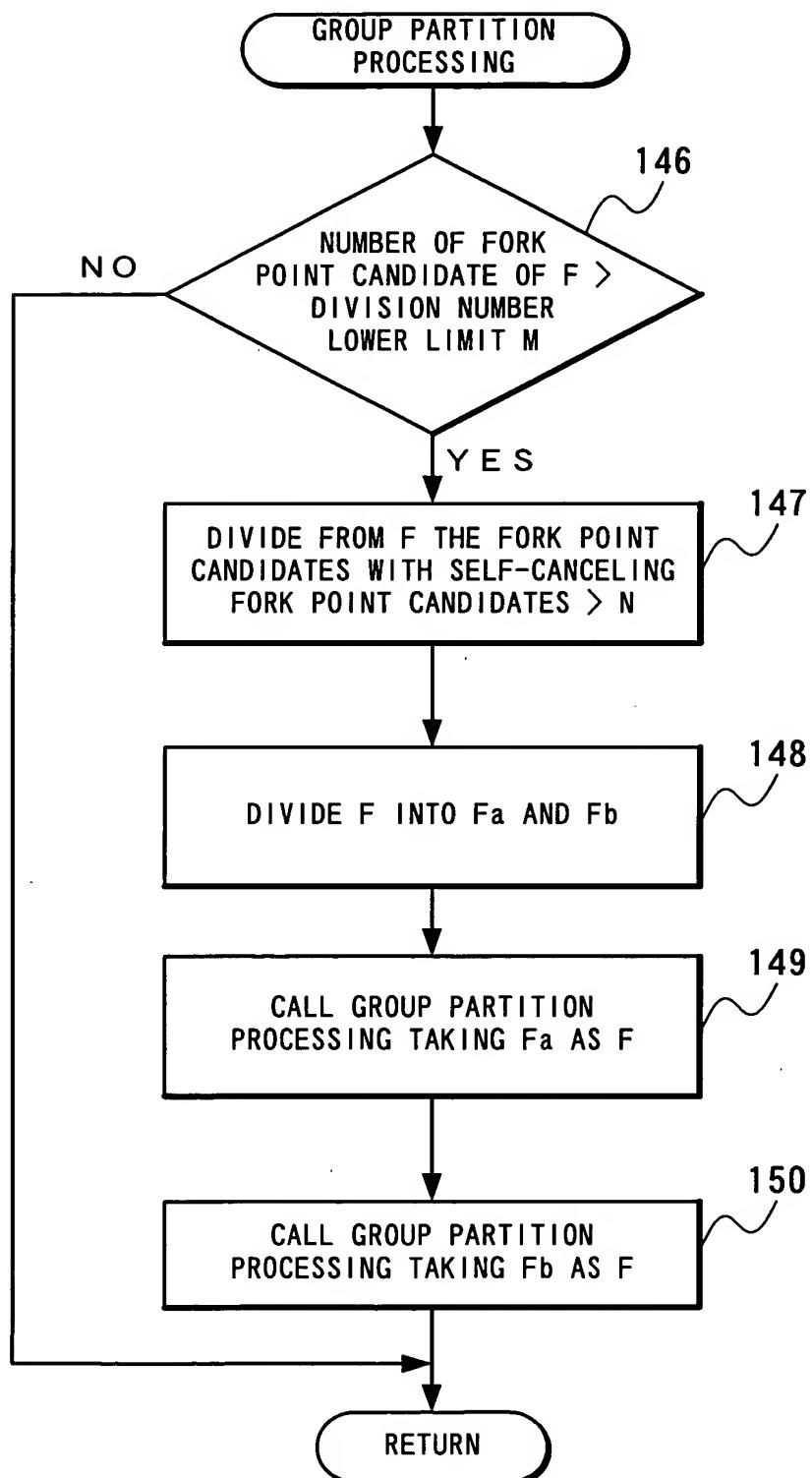


FIG. 17

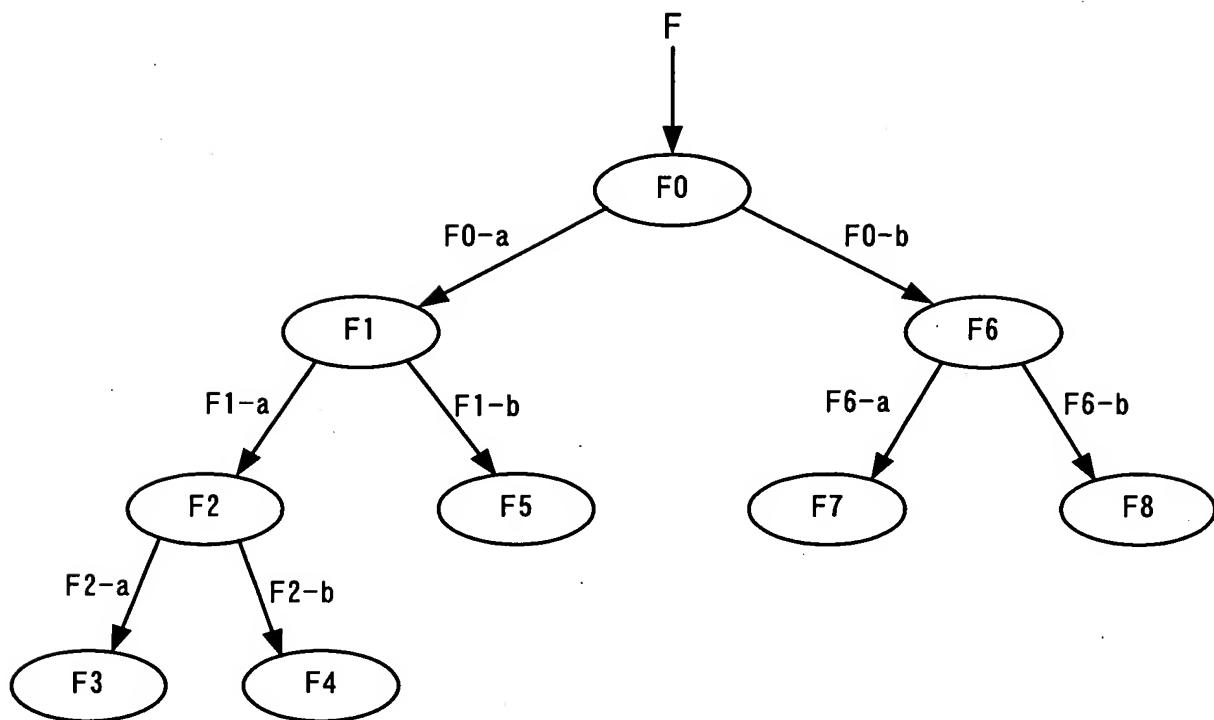


FIG. 18

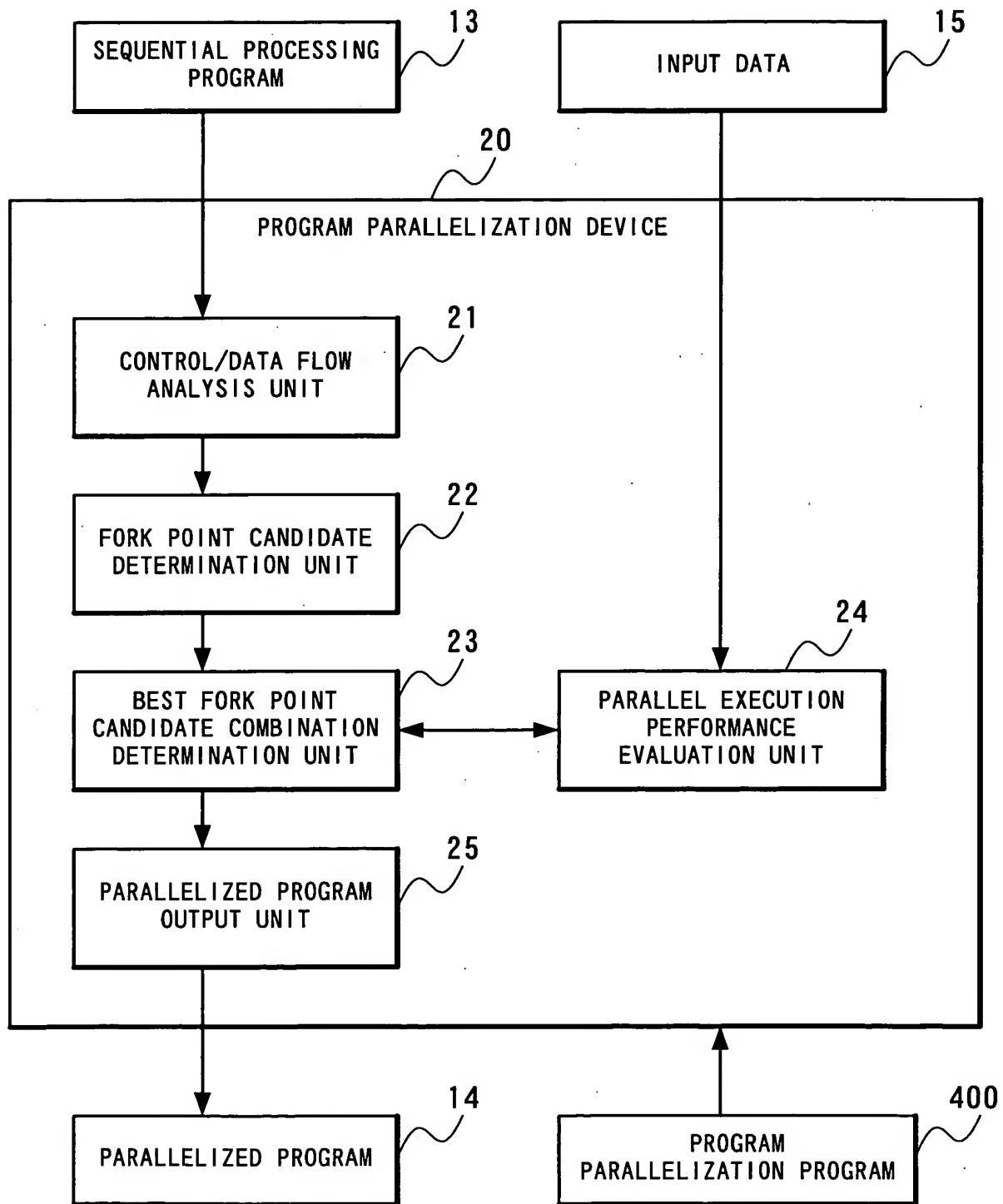


FIG. 19

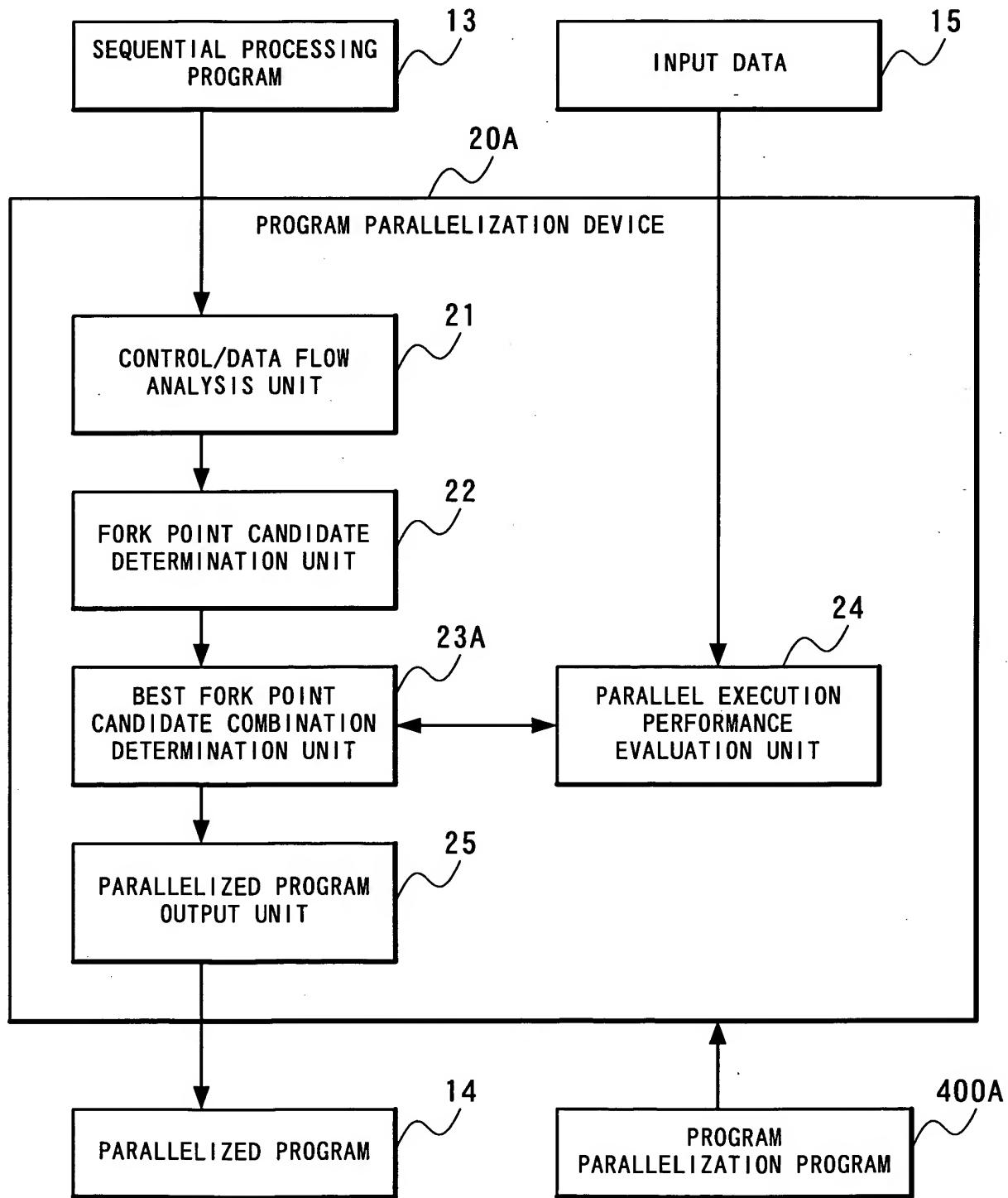
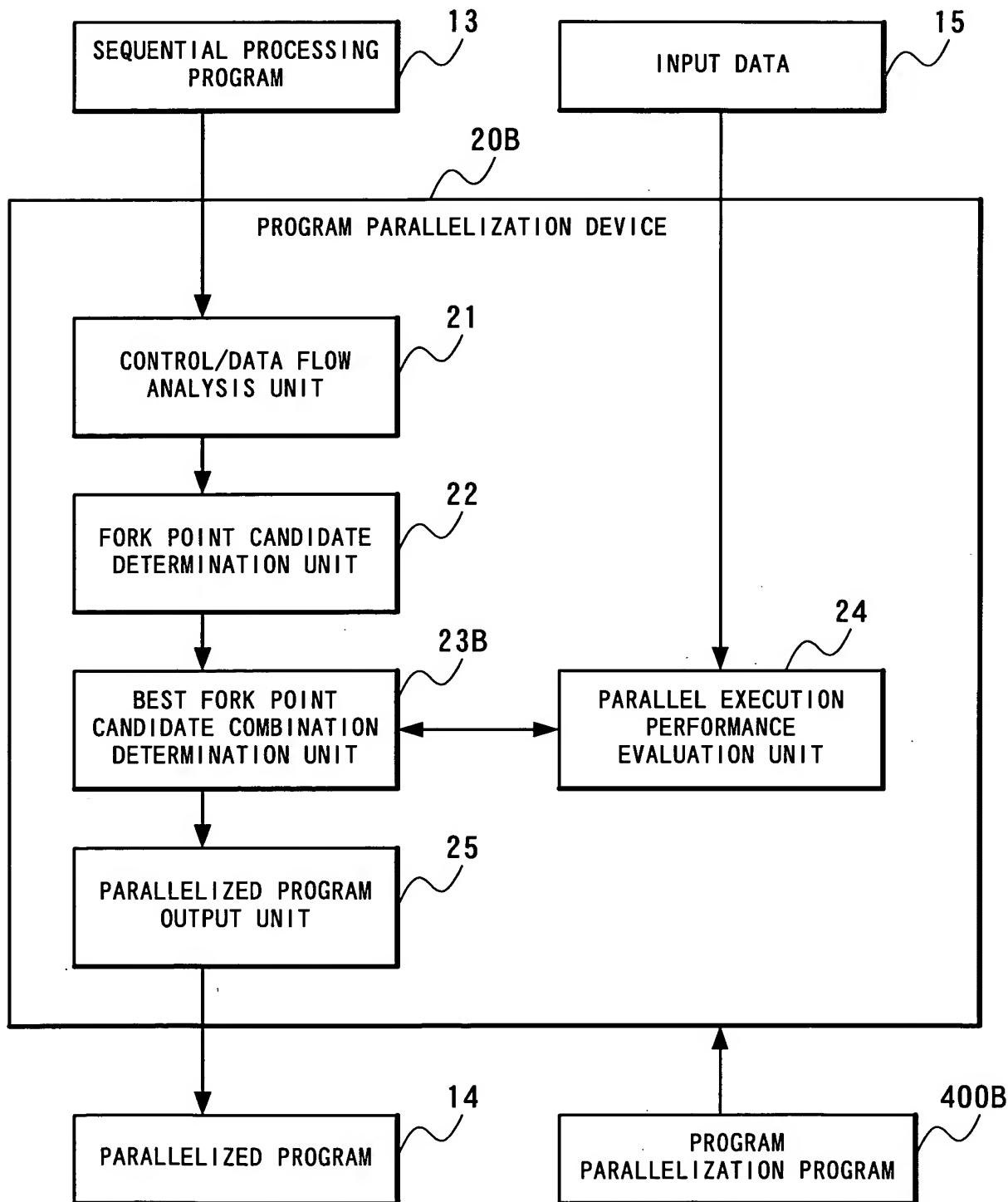
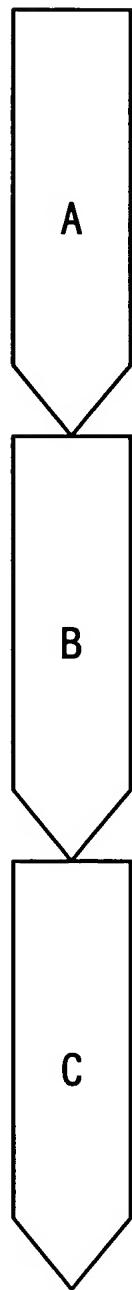


FIG. 20



Title: PROGRAM PARALLELIZATION
DEVICE, PROGRAM PARALLELIZATION
METHOD, AND PROGRAM
PARALLELIZATION PROGRAM
Inventor(s): Atsufumi SHIBAYAMA et al.
DOCKET NO.: 040405-0368

FIG. 21 (PRIOR ART)



Title: PROGRAM PARALLELIZATION
DEVICE, PROGRAM PARALLELIZATION
METHOD, AND PROGRAM
PARALLELIZATION PROGRAM
Inventor(s): Atsufumi SHIBAYAMA et al.
DOCKET NO.: 040405-0368

FIG. 22 (PRIOR ART)

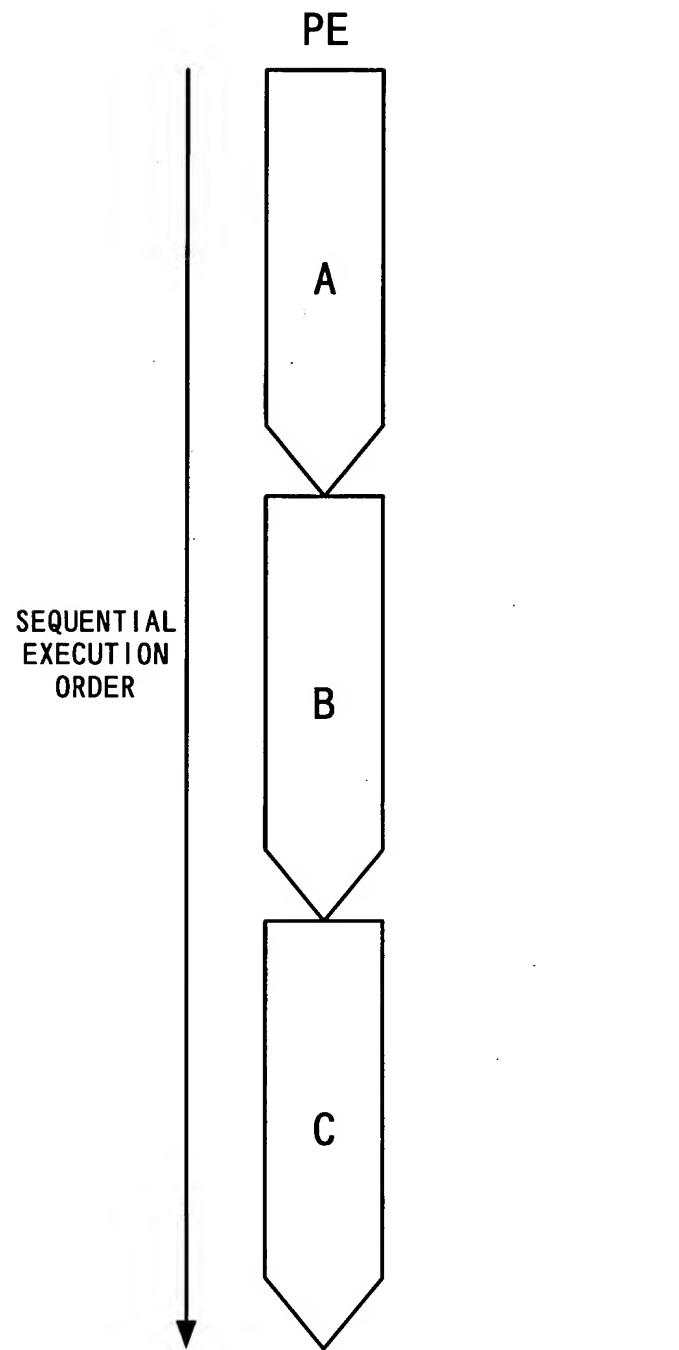


FIG. 23 (PRIOR ART)

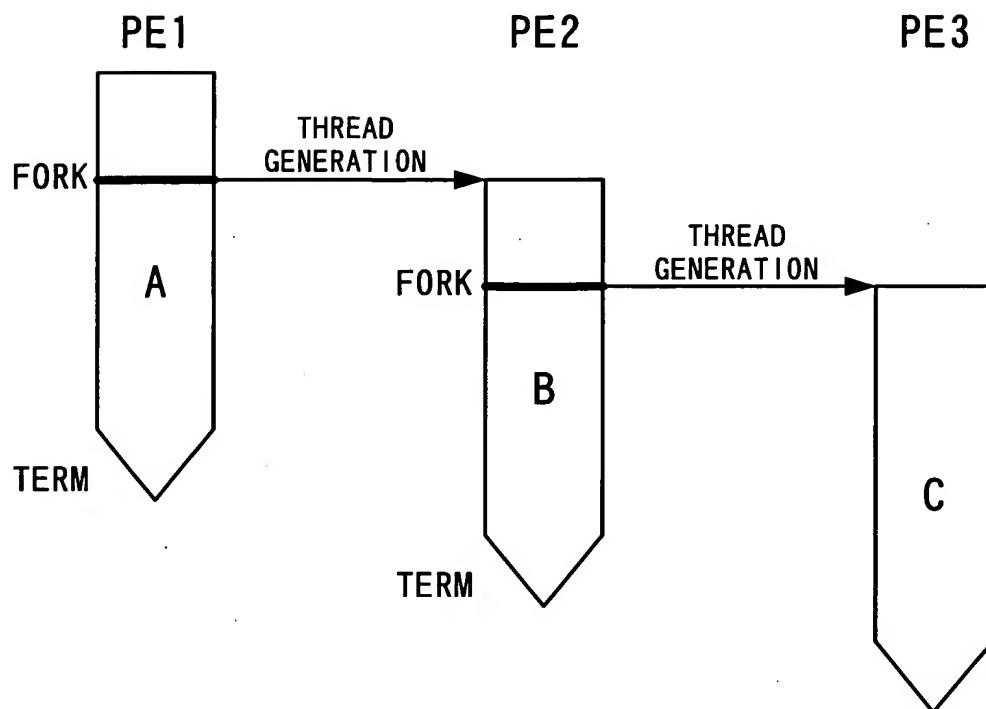


FIG. 24 (PRIOR ART)

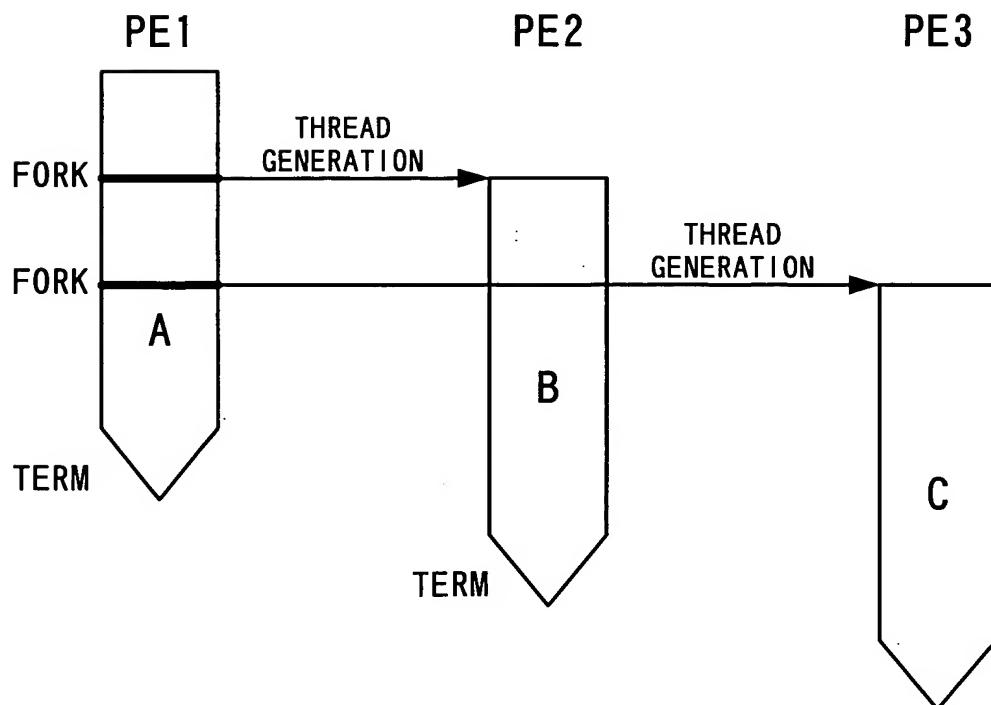


FIG. 25 (PRIOR ART)

